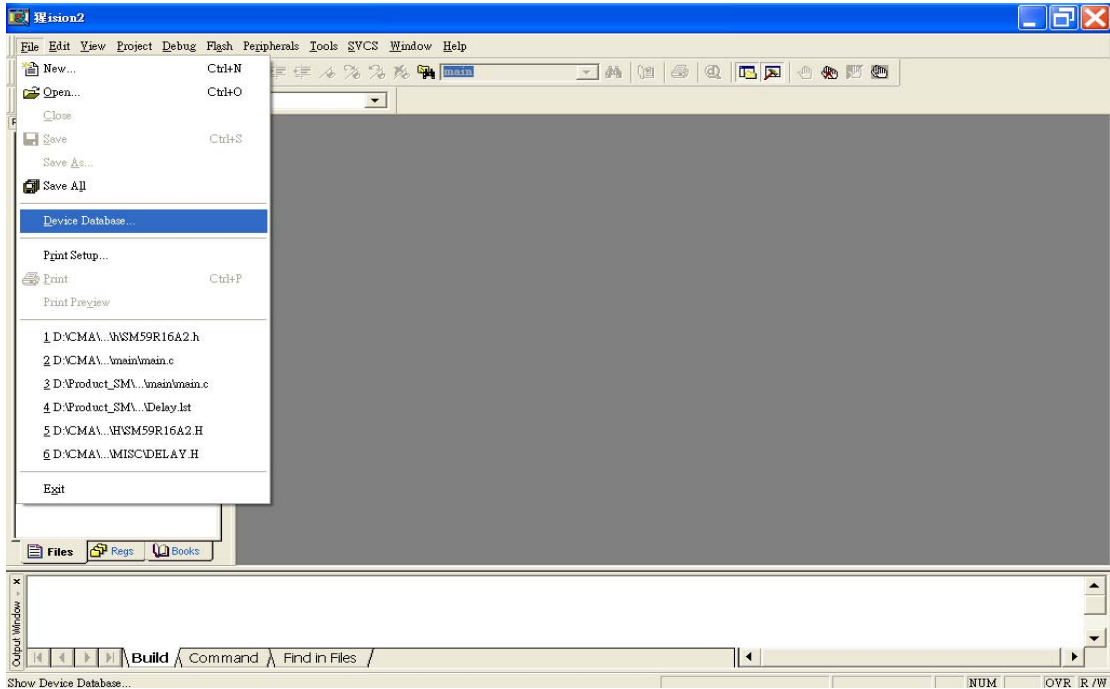




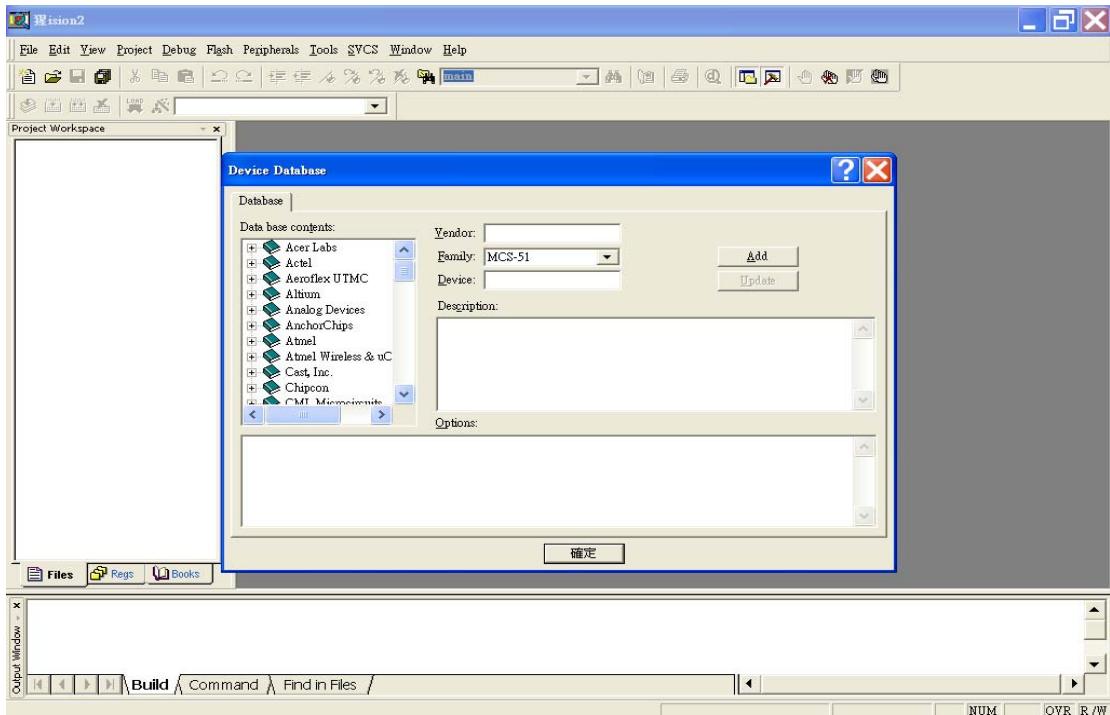
如何新增 Keil C Data Base 元件支援？

1. 點選 "File\Device Database"



2. 輸入項目 1. Vender 2. Device 3. Description 4.Options

3. 輸入完成後，點擊"Add"即可！





PS: 如何設定 Device Database "Options"，請參考下表說明：

Adjusting the Options...

In the Options box, **CPU=** specifies the basic tool settings for the chip. The parameters for **CPU=** are:

- **IRAM (range)**
Address location of the on-chip IRAM.
- **XRAM (range)**
Address location of the on-chip XRAM.
- **XRAM2 (range)**
Address location of a 2nd on-chip XRAM area (i.e. for C167CS).
- **IROM (range)**
Address location of the on-chip (flash) ROM.
For classic C166: start address must be 0; IROM is split into two sections if size is above 32KB; range specifies physical ROM size.
- **IROM2 (range)**
(for 8051 and variants only) Address location of 2nd on-chip ROM.
- **ICAN (range)**
Address location of the on-chip CAN module. Use for C167 and variants only.
- **CPUTYPE (variant)**
Specify CPU variant for ARM based controllers. Currently only ARM7 and ARM9 devices are supported.
- **EBIG**
Default to BIG endian for ARM based controllers.
- **ELITTLE**
Default to LITTLE endian for ARM based controllers.
- **ESEL**
Allow selection of the endianness for ARM based controllers.
- **CLOCK (val)**
Default CPU clock speed (in Hz) used when you select the device. For example CLOCK(12000000), indicates a 12MHz oscillator.
- **MDU_F120**
Use the Multiply/Accumulate Unit of SiliconLabs C8051F12x device variants.
- **MDU_R515**
Use the Multiply/Divide Unit of Cast/Evatronix R80515.
- **MOD167**
Use the extended instruction set of the C167 and variants.
- **MOD517DP**
Enable Infineon specific multiple DPTR registers.
- **MOD517AU**
Enable the Infineon specific Arithmetic Unit.
- **MODA2**
Enable Atmel specific multiple DPTR registers (like on AT89S8252).
- **MODAB2**
Enable Analog Devices specific multiple DPTR registers.
- **MODC2**
Enable Cast/Evatronix specific multiple DPTR registers (R80515).
- **MOD_CONT**
Enable support for the Dallas Contiguous Mode.



- **MODDA**
Enable Dallas specific Arithmetic Accelerator.
- **MODDP2**
Enable Dallas specific multiple DPTR registers.
- **MODH2**
Enable Hynix/ST uPSD33xx uPSD34xx multiple DPTR registers.
- **MODP2**
Enable Philips specific multiple DPTR registers. (Note also some Atmel devices are using this variant).
- **MODV2**
Use the Infineon XC16x / ST Super10 instruction set extensions.
- **MX**
Enable support for the Philips 80C51MX architecture.
- **MPX**
Enable support for the Philips SmartMX SmartCard architecture.
- **DPX**
Enable 24-bit DPTR register for the Analog Devices ADuC812.
- **PMW**
Enable the PCON.PMW feature that allows to use MOVX instructions to write into code space for the Evatronix R8051XC.
- **DPC**
Enable the data pointer control registers on the Evatronix R8051XC that provide auto-increment features for the DPTR registers.
- **BSE**
Enable the Bank Switch Enable feature in the register DPSEL.3 for the Evatronix R8051XC.
- **PSOC**
Enable the generation of interrupt vectors for Cypress PSoC.

The following additional options are also available:

- **REGFILE=*sfr_file(folder)***
Specify the SFR register definition file for the CPU.
- **SFILE=*startup_file(folder)***
Specify the default startup file that should be copied to a project.
- **FLASH=*command_file(options)***
Specify the Flash Utility that should be used with the device.

Other options specify data books and µVision debugging DLLs. You should leave these variables unchanged when adding a new device to the database.

以上資料參考至... <http://www.keil.com/support/docs/1421.htm>

4. 輸入錯誤怎麼辦？請直接修改，並點擊 Update；或直接選擇 Device，按鍵盤”DEL”即可！
(關於 Keil C 詳細操作，可參考：<http://www.keil.com/>)

